

FIG. 1

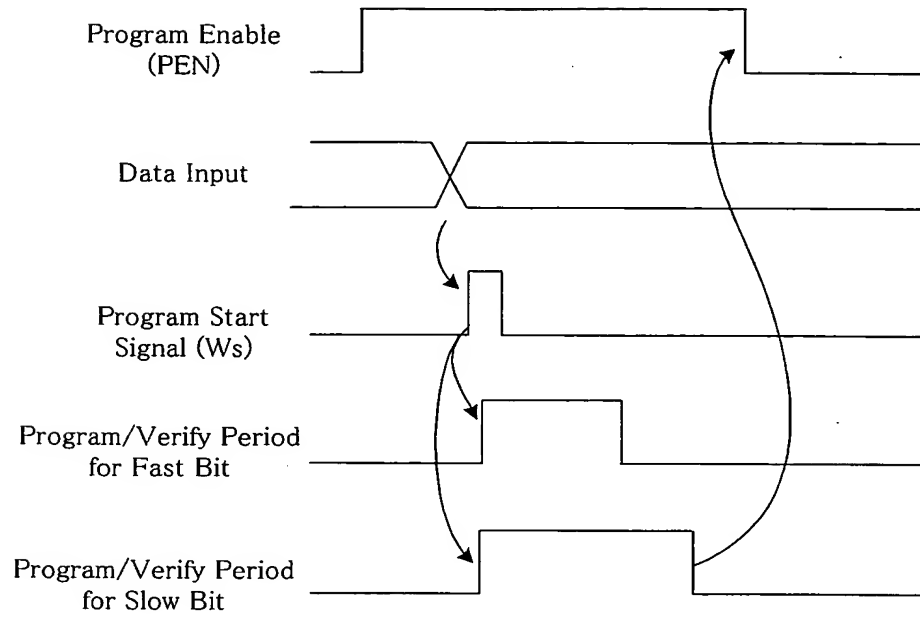
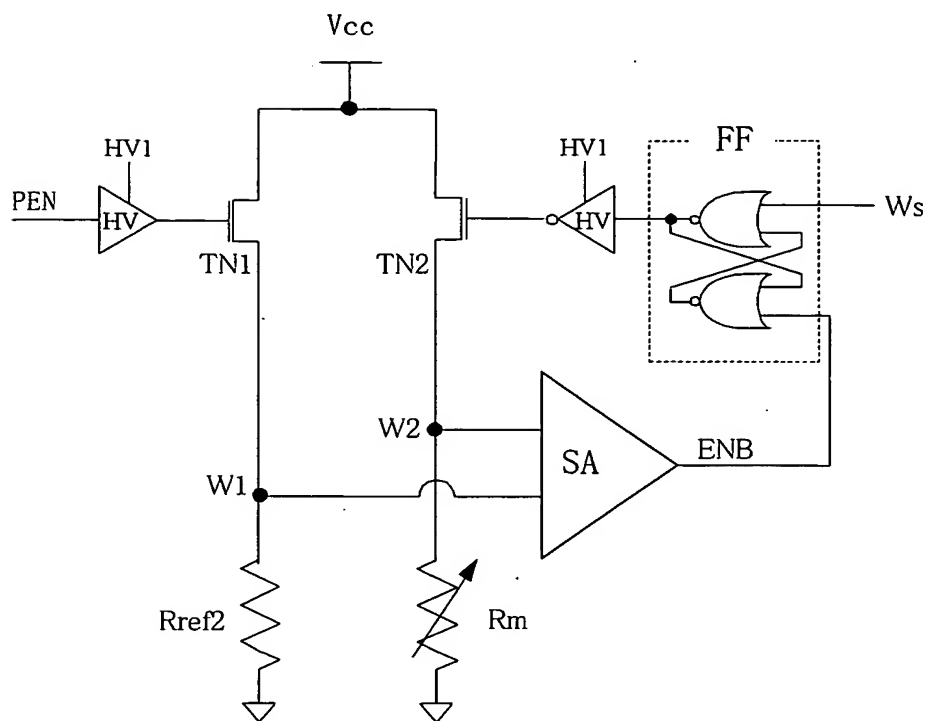


FIG. 2

**FIG. 3**

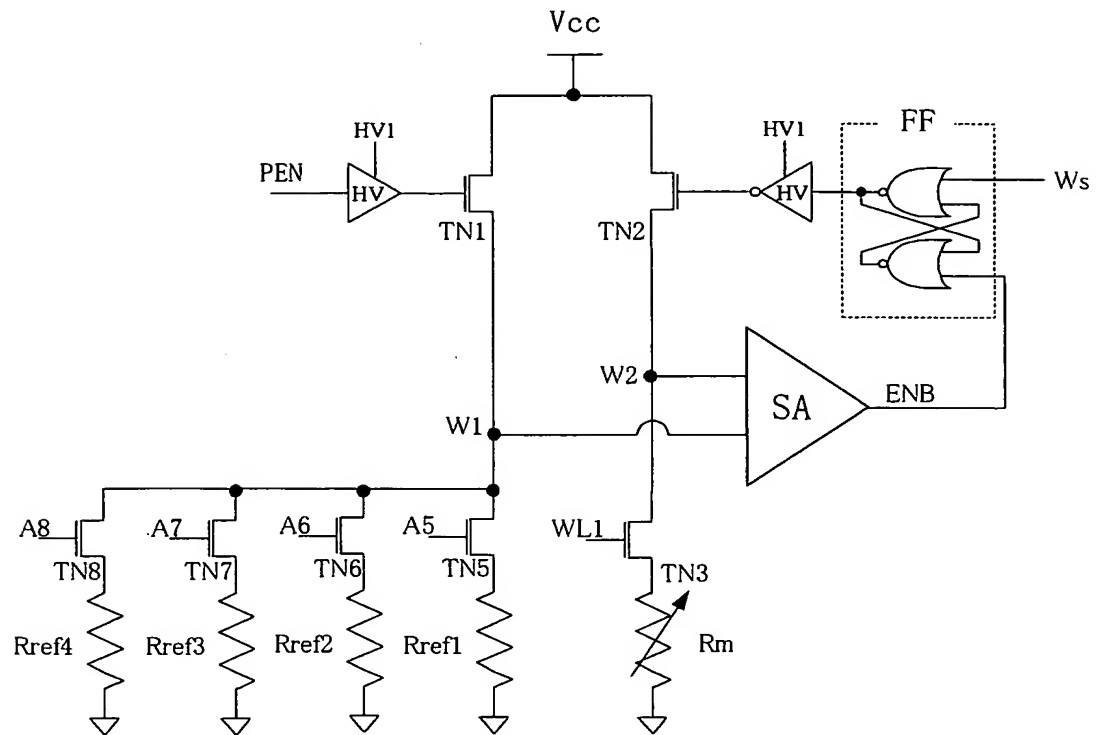


FIG. 4

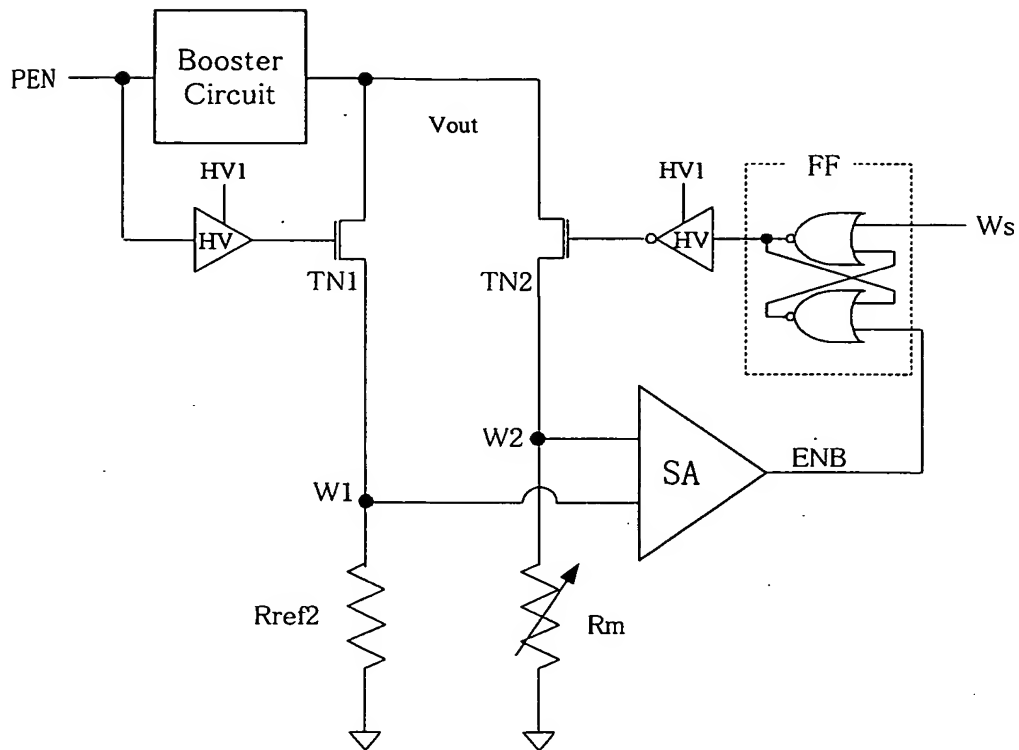


FIG. 5

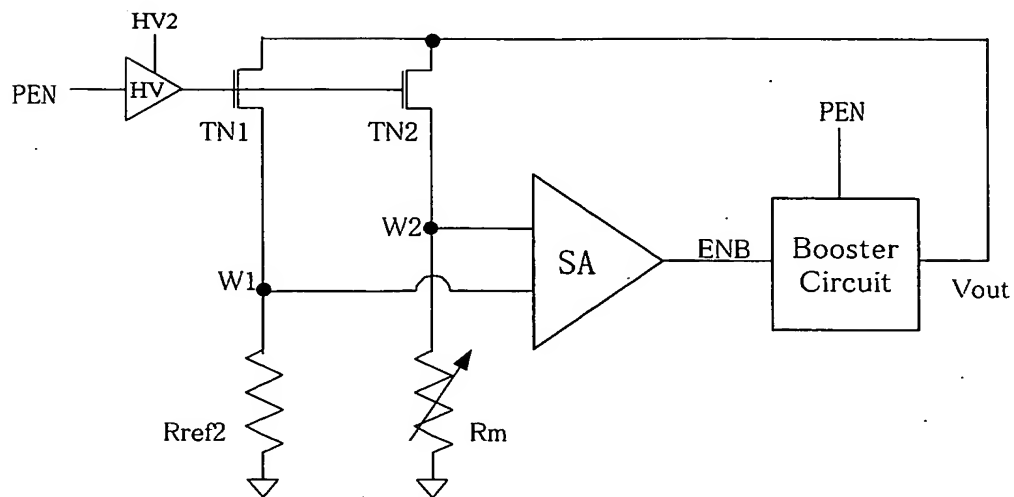


FIG. 6

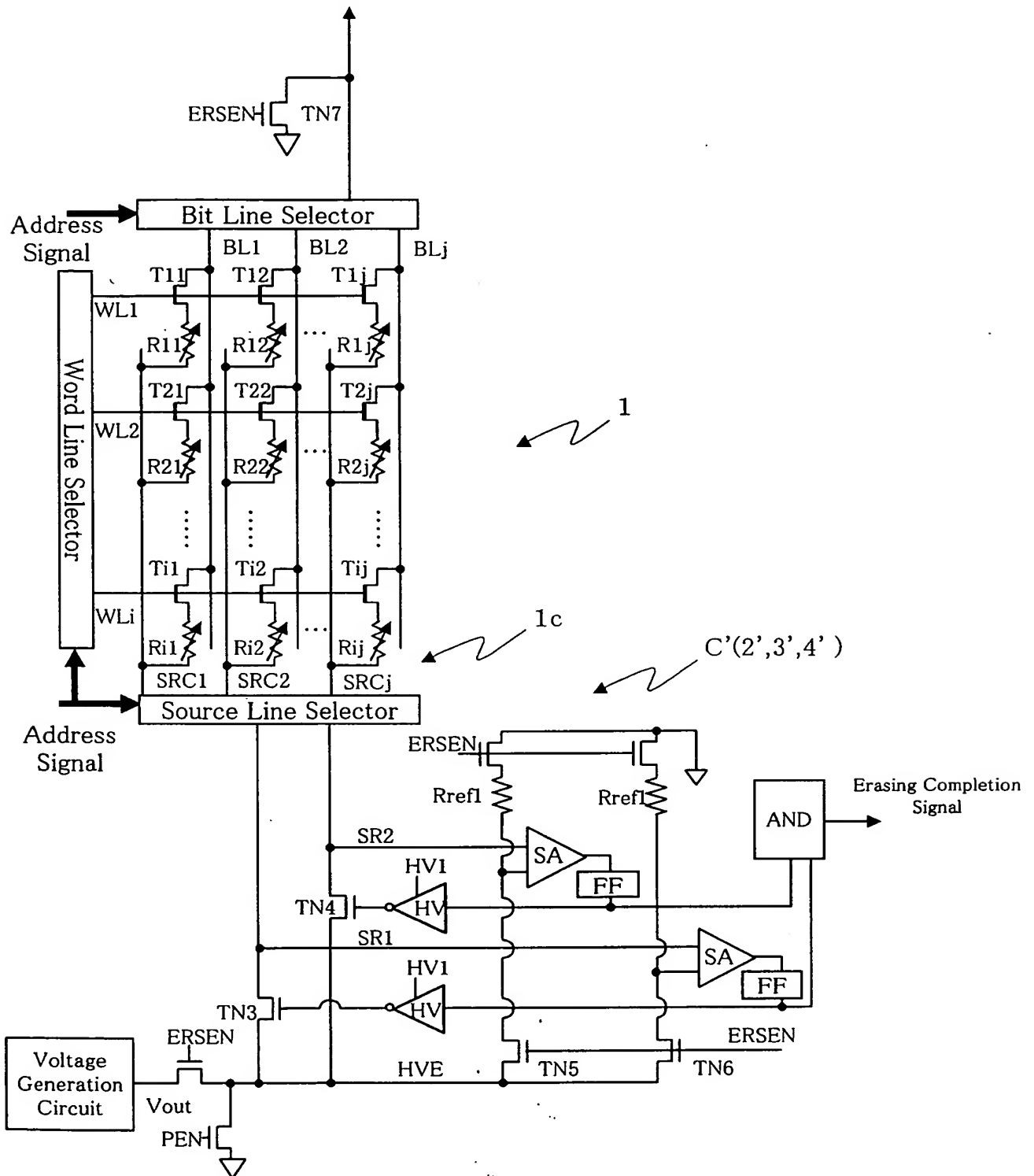


FIG. 7

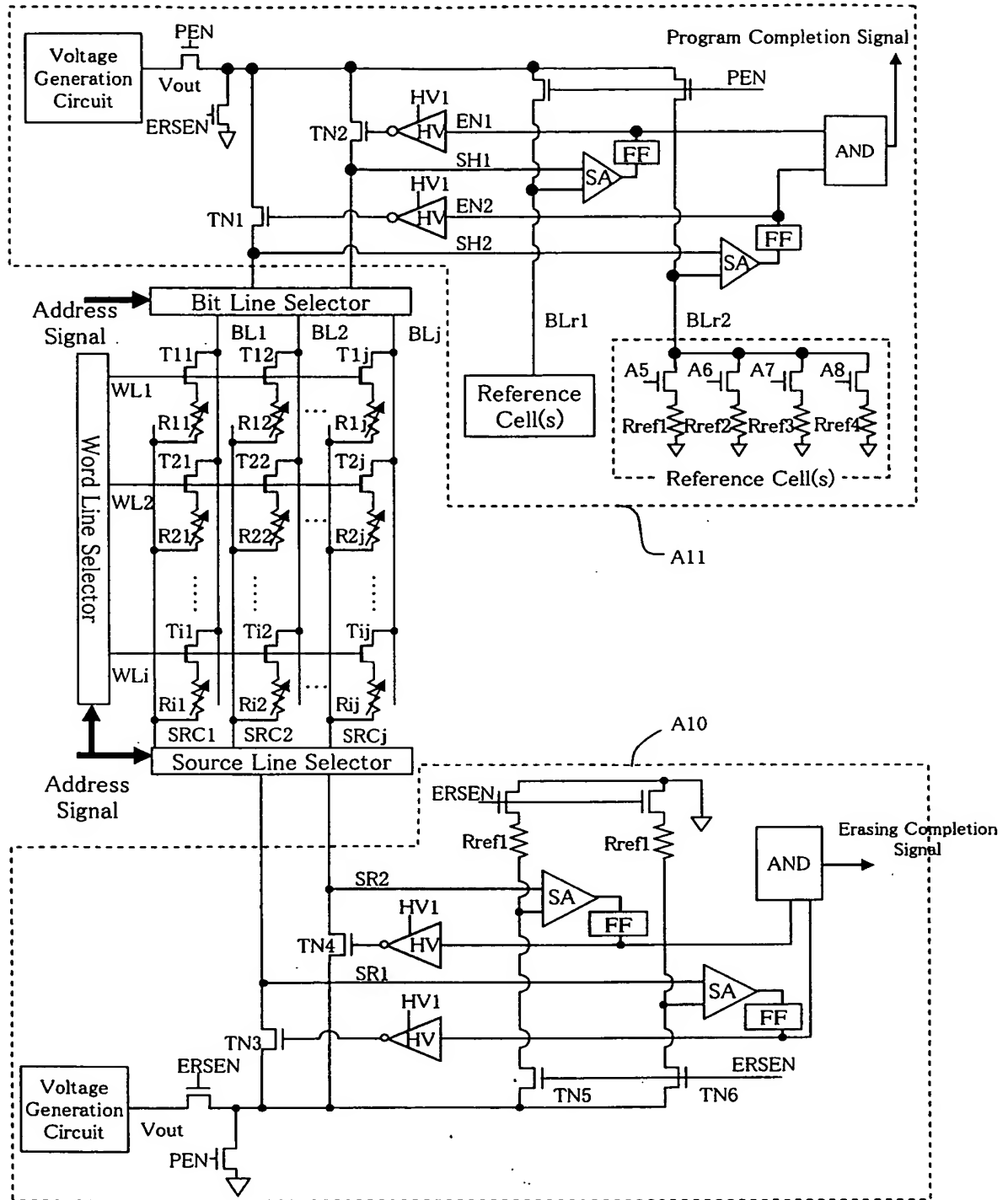


FIG. 8

Title: SEMICONDUCTOR MEMORY DEVICE AND METHOD
 FOR PROGRAMMING AND ERASING A MEMORY CELL

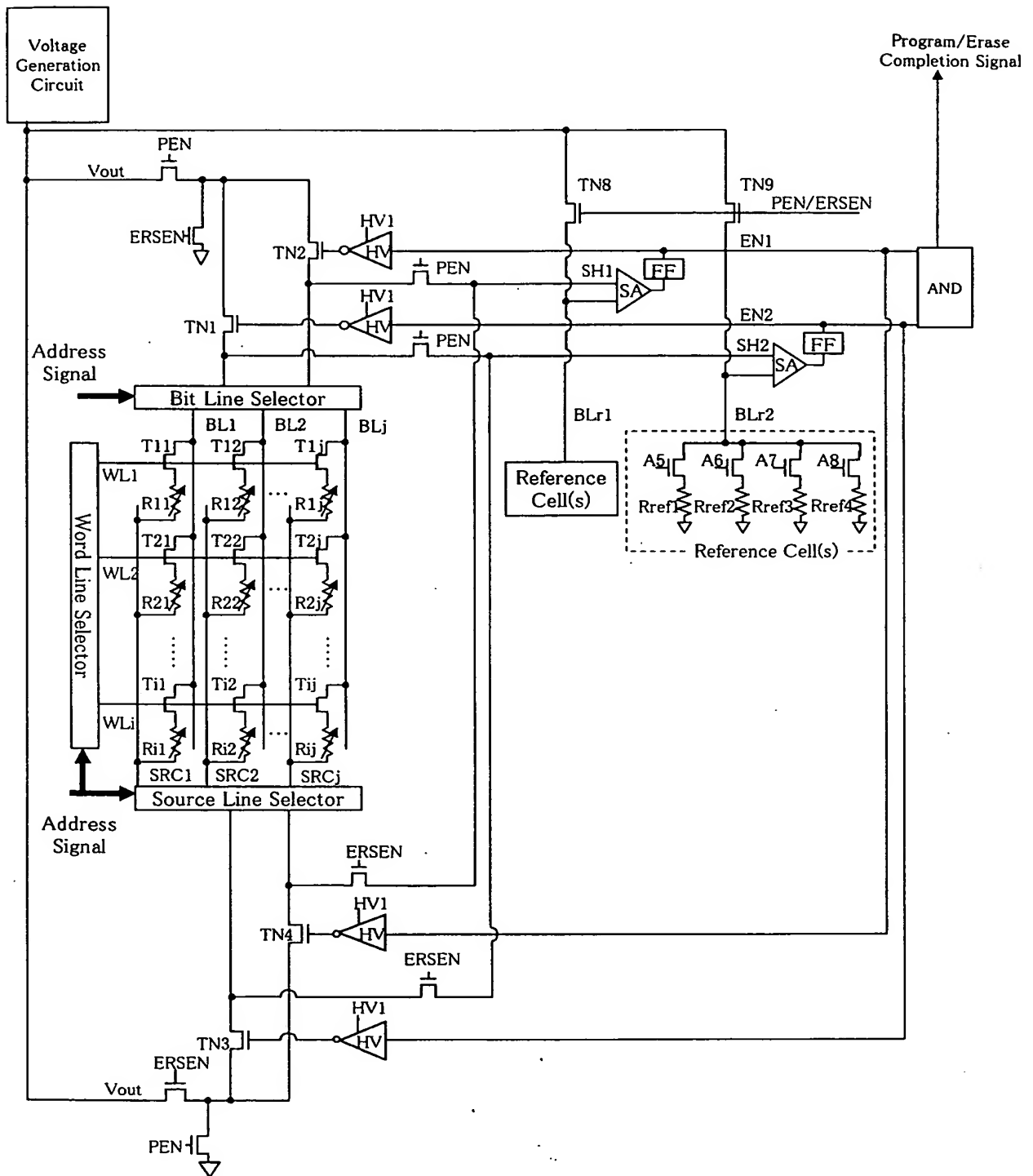


FIG. 9

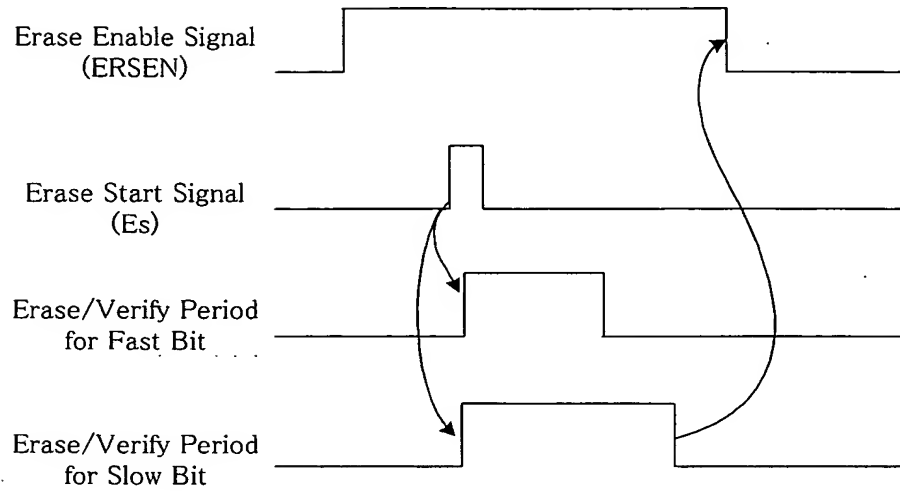


FIG. 10

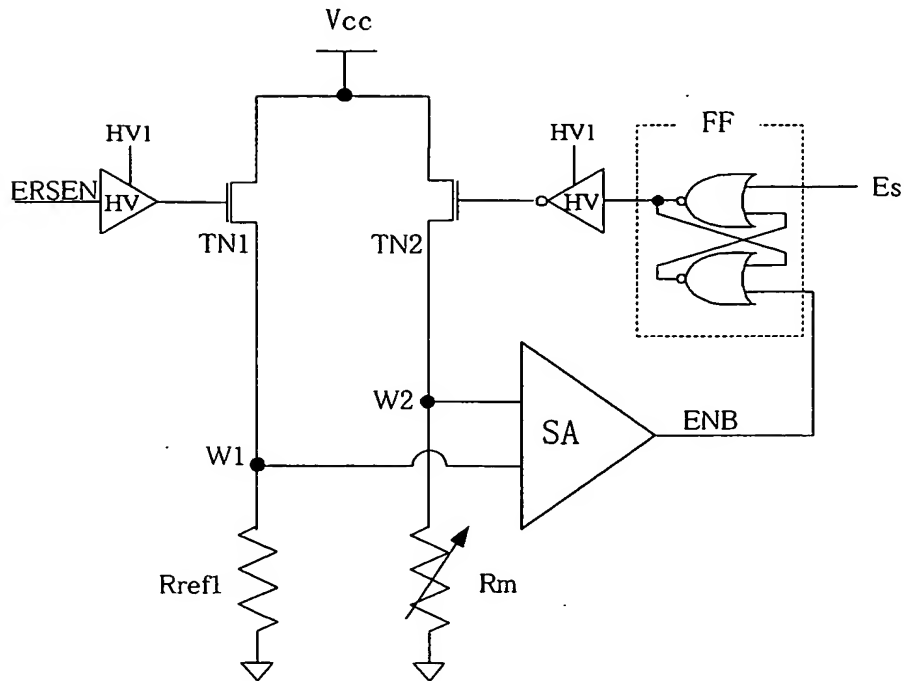


FIG. 11

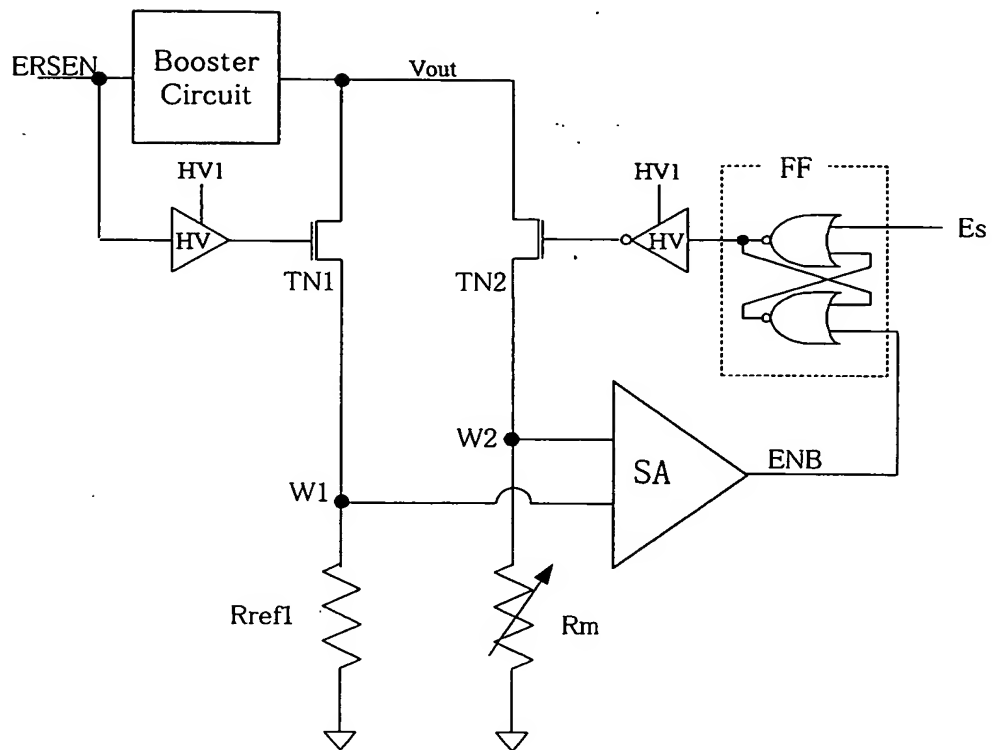
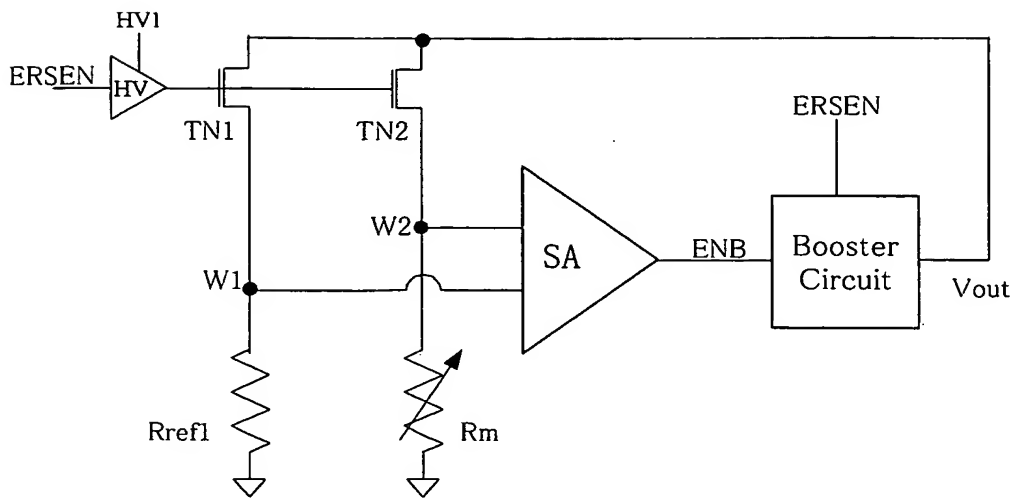


FIG. 12

**FIG. 13**

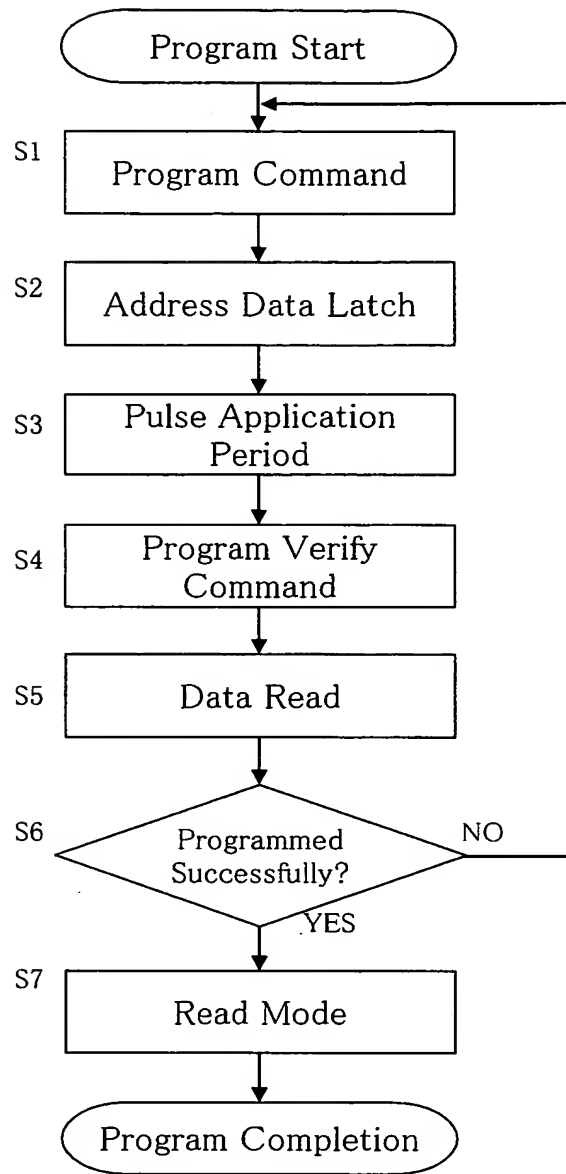


FIG. 14
PRIOR ART

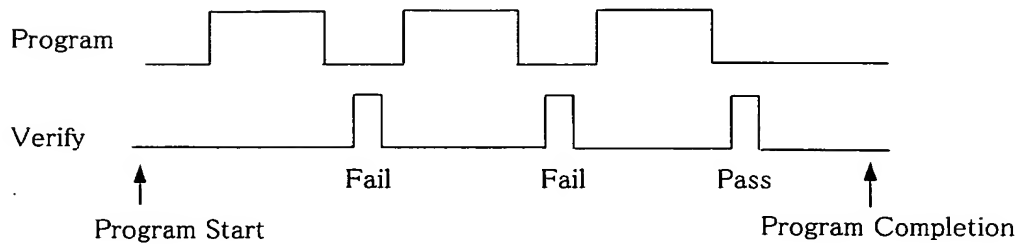


FIG. 15
PRIOR ART